

FIG. 1

1

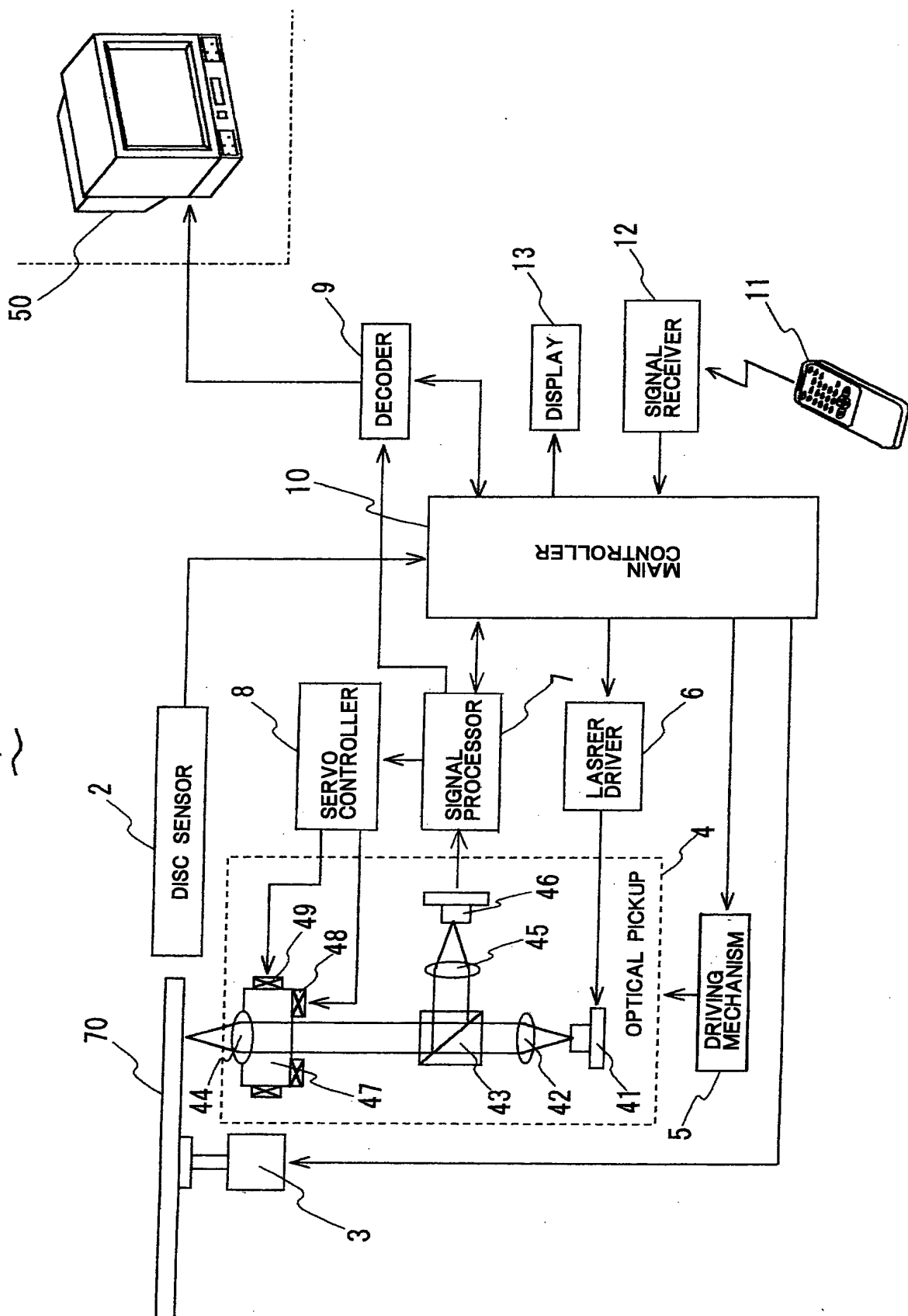


FIG. 2

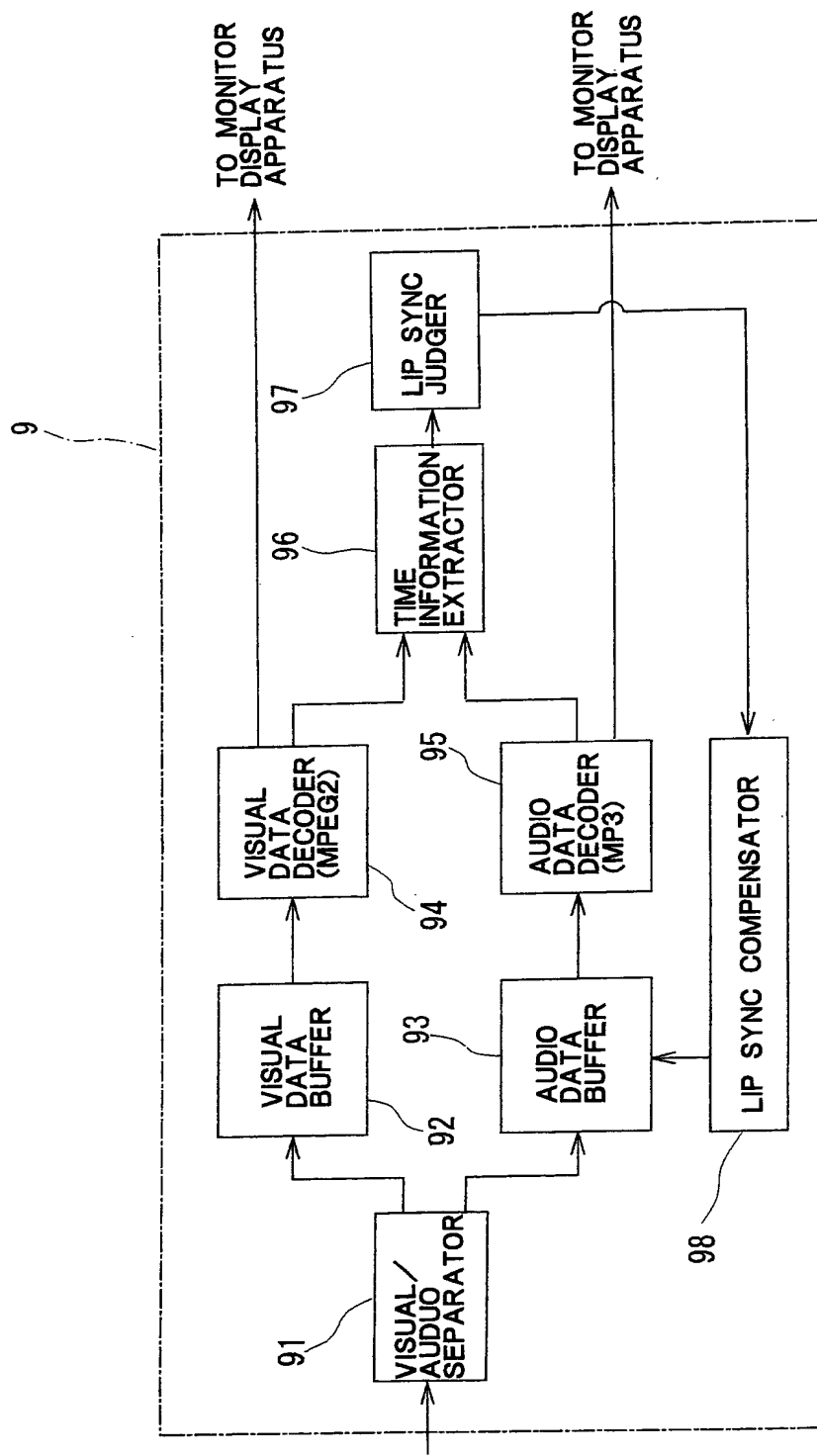


FIG. 3

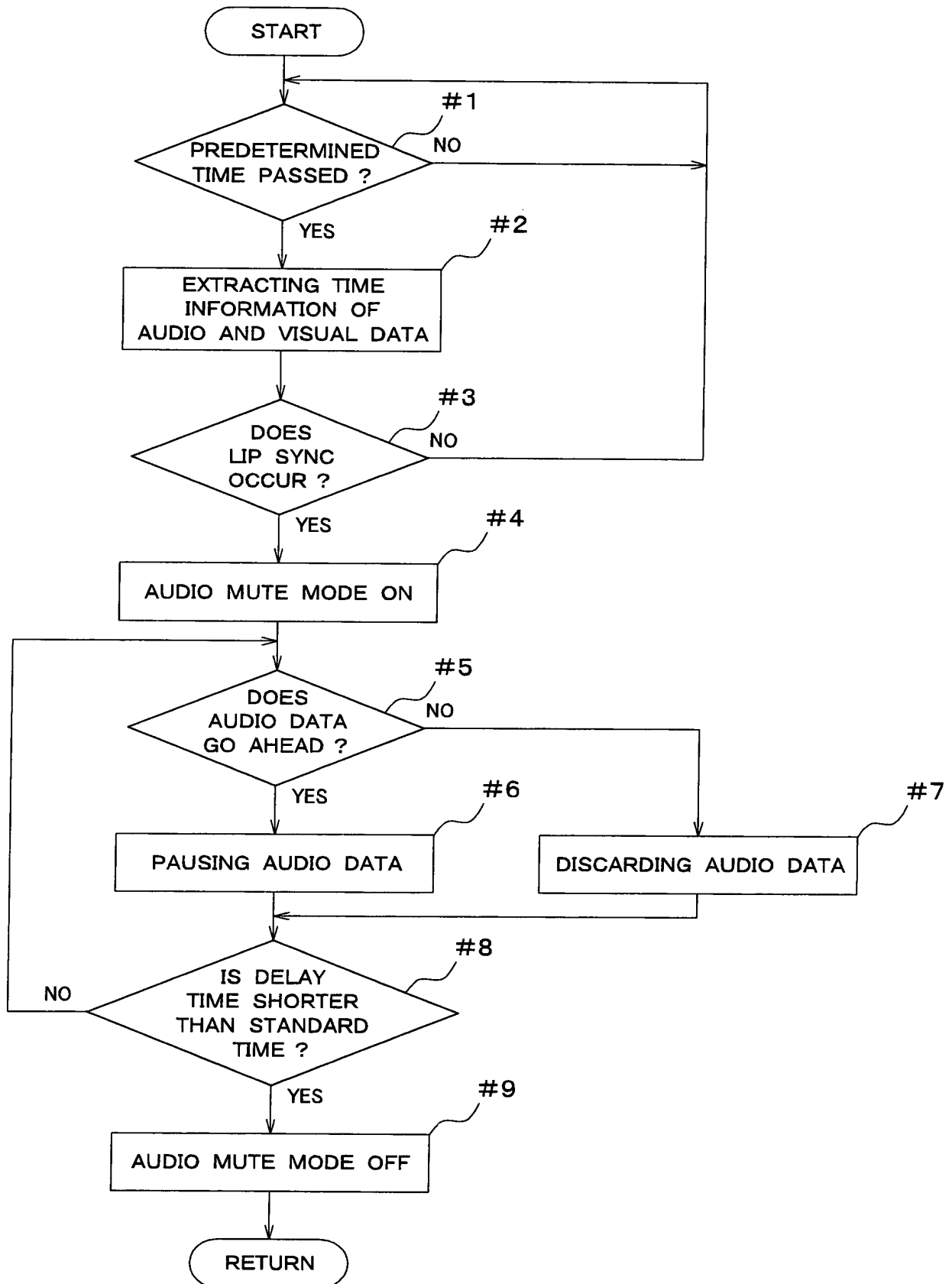
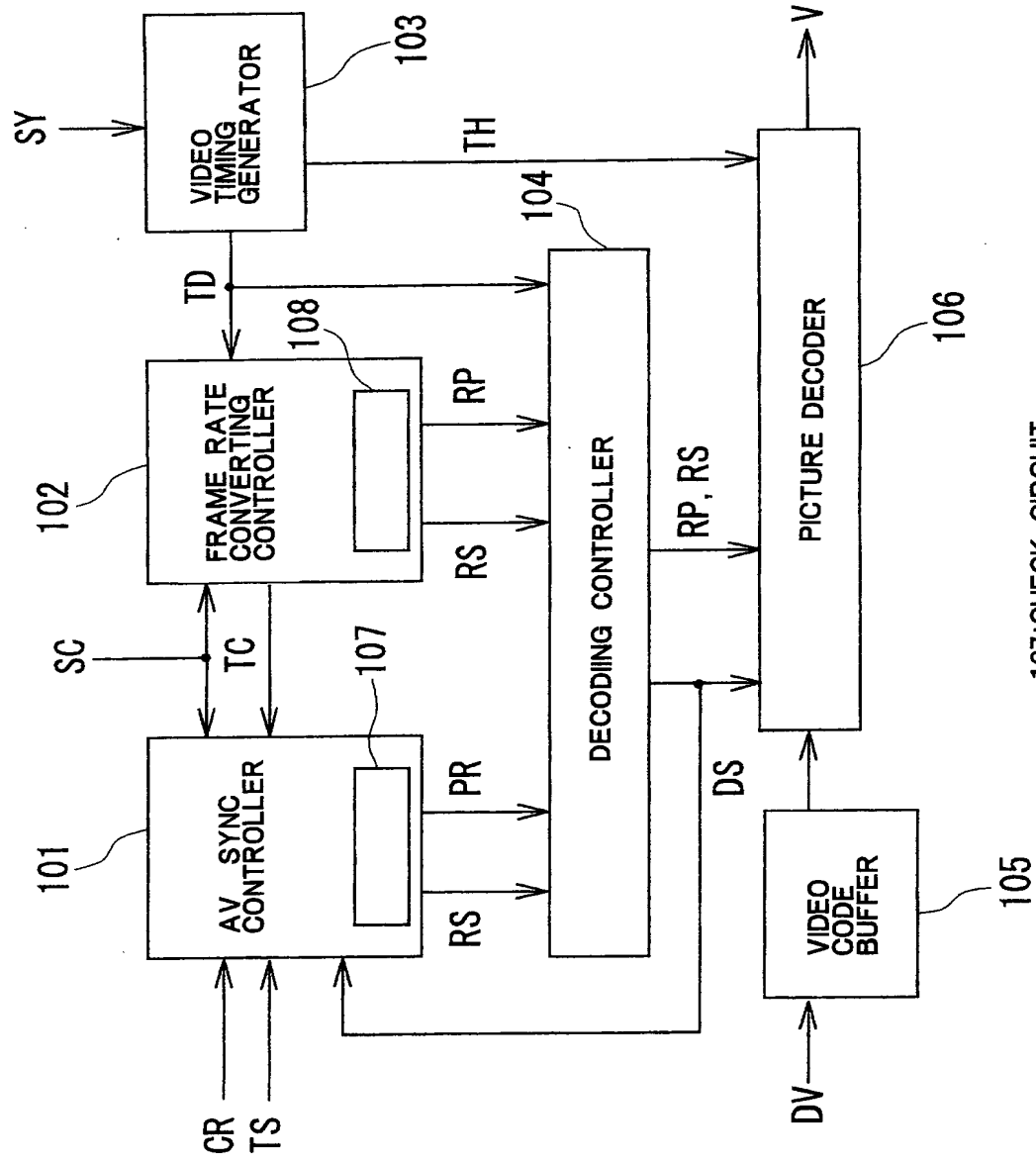


FIG. 4 (PRIOR ART)



107:CHECK CIRCUIT

108:CHECK SIGNAL GENERATOR